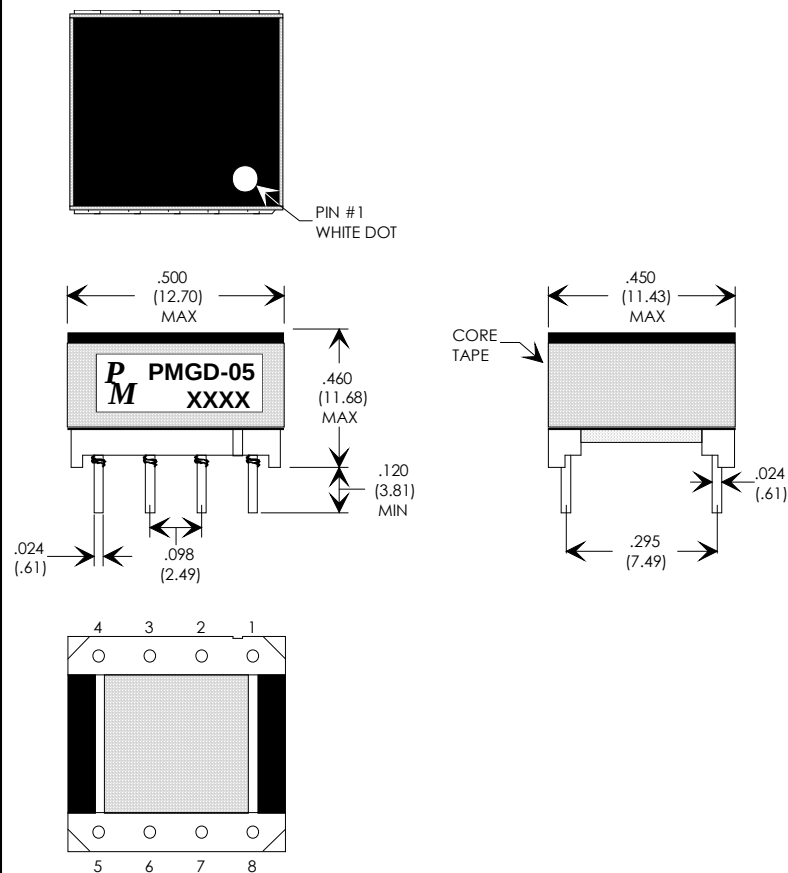
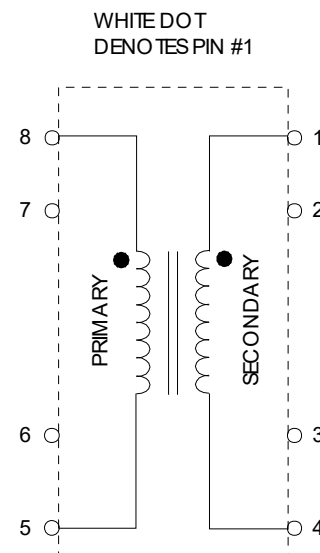
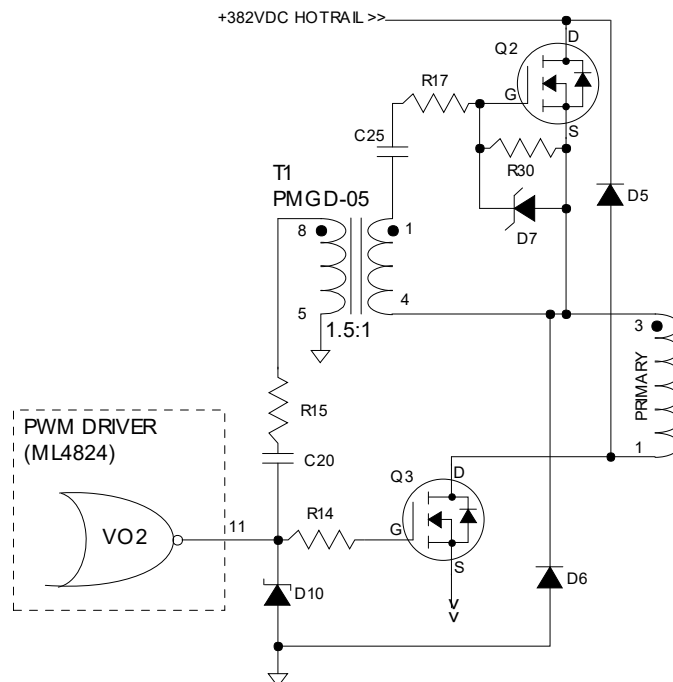


TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C

A) 1:0.67 GATE DRIVE TRANSFORMER.

PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
TURN RATIO'S: PRI : SECONDARY	-----	1:0.67	-----	± 2%
PRIMARY INDUCTANCE: (8 to 5) VOLTAGE = 0.200Vrms @ 10KHz	3.9	6.2	9.0	mHy
PRIMARY LEAKAGE IND: (8 to 5) VOLTAGE = 0.200Vrms @ 100KHz	-----	10.0	15.0	μHy
DCR: PRIMARY SECONDARY	----- -----	----- -----	1.0 0.8	Ohms Ohms
BIPOLAR DESIGN POINT: 15V Primary OPERATING FREQUENCY	-----	25	75	KHz
MAXIMUM DUTY CYCLE: BIPOLAR	-----	-----	50	%
MAXIMUM FLUX DENSITY: BIPOLAR	-----	-----	.15	Tesla
MAXIMUM FLUX DENSITY: UNIPOLAR	-----	-----	.20	Tesla
ET PRODUCT WITH <10% DROOP: ⁽¹⁾ (PRIMARY VOLTS x PULSE WIDTH)	-----	-----	-----	-----
BIPOLAR PULSE @ Bmax_Bipolar	-----	260	300	V-μsec
UNIPOLAR PULSE @ Bmax = Unipolar	-----	160	200	V-μsec
HI-POT: PRIMARY TO SECONDARY	1000	-----	-----	Vdc

(1) Higher ET's are possible at lower operating frequencies and/or if increased pulse droop can be tolerated.

FIGURE 2: PHYSICAL DIMENSIONS IN INCHES. (mm)**FIGURE 1: SCHEMATIC DIAGRAM****FIGURE 3: TYPICAL APPLICATION CIRCUIT**

EP10

REV.	DESCRIPTION OF CHANGES
12/06/95	INITIAL RELEASE
04/12/96	CORRECTED RATIO ERROR TABLE 1, NO OTHER CHANGES
09/23/98	CORRECTED PIN-OUT TO MATCH FIGURE 1
GATE DRIVE TRANSFORMER CONTROL DRAWING	
PREMIER P/N: PMGD-05	REVISION: 09/23/98
DRAWN BY: TOM O'NEIL	REF:
SCALE: NONE	SHEET: 1 OF 2