## **TABLE 1:** ELECTRICAL SPECIFICATIONS AT 25 °C SWITCHING TRANSFORMER DESIGNED FOR USE WITH POWER INTEGRATIONS PWR-TOP210PFI REFER TO APPLICATION CIRCUIT OF FIGURE 3.

DADAMETED				
PRIMARY INDUCTANCE (2-1) VOLTAGE = 0.250Vrms FREQUENCY = 100 KHZ	3.40	4.00	4.60	mHY
TURN RATIO'S: SECONDARY (8-5) : PRIMARY (2-1) BIAS (4-3) : PRIMARY (2-1)		1:11.17 1:26.80		<u>+</u> 4% <u>+</u> 4%
PRI LEAKAGE IND. (8-5 SHORTED) VOLTAGE = 0.250Vrms FREQUENCY = 100 KHZ		83.0	100.0	μHY
HIPOT: PRIMARY TO SECONDARY BIAS TO SECONDARY	3000 3000			Vrms Vrms
APP CIRCUIT PARAMETERS: (1) AC LINE VOLTAGE 47/400 Hz OUTPUT VOLTAGE NOMINAL OUTPUT CURRENT CONTINUOUS (2) OUTPUT CURRENT PEAK LINE REGULATION (102 TO 318Vac) LOAD REGULATION 10-100% RIPPLE	85 20 	15.0 3.00 6.00 20.0	265 200 280	Vac Vdc mA mA ±% ±% ±mV

FIGURE 1: SCHEMATIC DIAGRAM



## NOTE1:

REINFORCED INSULATION SYSTEM, UL1950, IEC950, CSA-950: A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS B) TRIPLE BASIC INSULATED SECONDARY. C) DESIGNED TO MEET ≥5.0mm CREEPAGE REQUIREMENTS. D) VARNISH FINISHED ASSEMBLY.

E) UL CLASS (B) 130 INSULATION SYSTEM PM130-R1, PM130-H1, PM130-H1A (UL FILE #E177139) OR ANY UL AUTHORIZED CLASS (B) INSULATION SYSTEM.

(1) REFER TO APPLICATION CIRCUIT OF FIGURE 3.

(2) WITH R3 OPTIONAL CLAMP RESISTOR IN PLACE

# FIGURE 2: PHYSICAL DIMENSIONS mm (INCHES)



# **APPLICATION NOTES**

Premier Magnetic's TSD-1330 Switch Mode Transformer was designed for use with Power Integrations, Inc. PWR-TOP210PFI three terminal off-line PWM switching regulator in the Flyback Buck-Boost circuit configuration. This conversion topology can provide isolated multiple outputs with efficiencies up to 90%. Premier'S TSD-1330 transformer has been optimized to provide maximum power throughput.

The PWR-TOPXXX series from Power Integrations, Inc. are self contained 100KHz three terminal voltage controlled PWM switching regulators. This series contains all necessary functions for an off-line switched mode control DC power source. These switching regulators provide a very simple solution to off-line designs. The inductors and transformer used with the PWR-TOPXXX are critical to the performance of the circuit. They define the overall efficiency, output power and overall physical size.

Below is a universal input, 3 watt application circuit utilizing Power Integrations PWR-TOP210 switching regulator in the flyback buck-boost configuration. This circuit provides +15Vdc at 200mA continuous and is capable of 300mA peak for short periods of time. This circuit represents the lowest cost implementation and utilizes the bias winding for feedback control. As such the line & load regulation are worse than that which could be achieved by utilizing an opto-coupler to sense the actual outputs. The component values listed are intended for reference purposes only. Resistor R1 may be adjusted up to 100 Ohms and down to 10 Ohms. As R1 increases in value the output voltage will increase, and vice-versa, thus allowing some fine adjustment on the initial output voltage. The EMI/RFI capacitors C7 & C8 are shown for reference but may not be needed to meet EMI/RFI emmision specifications. Clamp resistor R3 is recommended to stabalize the circuit during a no load condition.

#### PREMIER MAGNETICS PART NUMBERS: ALUMINUM ELECTROLYTIC FILTER CAPACITOR RATINGS: (REQUEST DATA SHEETS BY PART#) L1 = PMCU-0330 33mHy EMI/RFI CMC +15V OUTPUT: C2 ≥25V, Ripple Rated ≥ 250mA @ 100KHz @ Max. Op. Temp. T1 = TSD-1330 MAIN SWITCHING TRANSFORMER PANASONIC FA SERIES: LOW IMPEDANCE LONG LIFE RADIAL SERIES L2 = VTP-01001 100Hy, 1.0Amp INDUCTOR C2 = 220uF, 25V = PANASONIC ECA1EFG221 C3 = 100uF, 25V = PANASONIC ECA1EFG101 BR1 EMI FILTER 0.75A -DF06S HOT RAIL 11 600V, 1.0A 250v +110 to 375Vdc PMCU-0330 + C1. 22.0 UE. 400V 33mHY 2 Nichicon # UV\$2G220MHA, 85C **∖●**) CI Diameter=16mm (.630) 22 UF 85-265Vac C6 50/60Hz Length=25mm (.984) 0.1uF 4 NEUT 3 C6 = 0.1uF 250Vac X2-Saftey Cap CLAMP NETWORK 1 L2 VR1 VTP-01001 D3 P6KE250A MBR160 10uH @ 1.0A PRI 10 +15Vdc $\gamma\gamma$ 0 D1 @ 200mA MUR160 2 R2<sup>(1)</sup> C2 C.3 330 100uF 220uF R 1 D2 1 W 25V 25V $22\Omega$ 1N4148 SEC 5 -O RTN 6 NOTE (1) R3 CLAMP RESISTOR REQUIRED IF A NO LOAD CONDITION BIAS CAN OCCUR AT OUTPUT U1 4 POWER INT T 1 PWR-TOP210PF TSD-1330 DRAIN CONTROL C -N/C N/C Ш $\neg$ N/C N/C C 5 -----47 u F C7 & C8 = 250Vac Y-SAFTEY CAPACITORS 10V SOURCE EMI FILTER C7 C8 2.2nF 2.2n F HIGH VOLTAGE RETURN UNLESS OTHERWISE SPECIFIED TRANSFORMER CONTROL DRAWING Premier DIMENSIONS ARE IN MM PREMIER P/N: TSD-1330 DIMENSIONAL TOLERANCES ARE: **REVISION: 09/17/99** DECIMALS ANGLES Magnetics Inc. <u>+</u>0 ° .X <u>+</u> .25 .XX <u>+</u> .15 30 ENGR: PETER PHAM **REF: PWR-TOP210PFI**

SCALE: NONE

SHEET: 2 OF 6

DO NOT SCALE DRAWING

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# FIGURE 3: TYPICAL APPLICATION CIRCUIT