

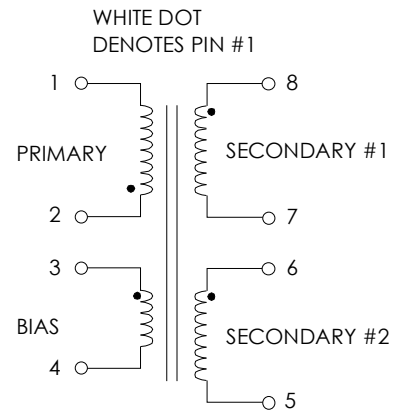
TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C

SWITCHING TRANSFORMER DESIGNED FOR USE WITH POWER INTEGRATIONS PWR-TOP202YAI. REFER TO APPLICATION CIRCUIT OF FIGURE 3.

PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
PRIMARY INDUCTANCE (2-1) VOLTAGE = 0.250Vrms FREQUENCY = 100 KHZ	450	500	550	μHY
TURN RATIO'S: SEC #1 (8-7) : PRIMARY (2-1) SEC #2 (6-5) : PRIMARY (2-1) BIAS (3-4) : PRIMARY (2-1)	-----	1: 3.375 ----- 1: 13.50 ----- 1: 6.750	-----	± 3% ----- ± 3% ----- ± 3%
PRI LEAKAGE IND. (SEC'S SHORT) 0.250Vrms @ 100KHz	-----	-----	45.0	μHY
HIPOT: PRIMARY & BIAS TO SECONDARY'S SECONDARY #1 TO SECONDARY #2	3000 1500	----- -----	----- -----	Vrms Vrms
APP CIRCUIT PARAMETERS: ⁽¹⁾ AC LINE VOLTAGE 47/400 Hz SEC #1 OUTPUT VOLTAGE SEC #1 OUTPUT mA CONTINUOUS SEC #2 OUTPUT VOLTAGE SEC #2 OUTPUT mA CONTINUOUS LINE REGULATION (85 TO 265Vac) RIPPLE EACH OUTPUT TRANSFORMER TEMPERATURE RISE	85 20 200 ----- ----- -----	----- 24.0 ----- 5.0 ----- 0.20 ----- 100.0 ----- 35.0	265 500 1000 ----- ----- -----	Vac Vdc mA Vdc mA ±% ±mV °C

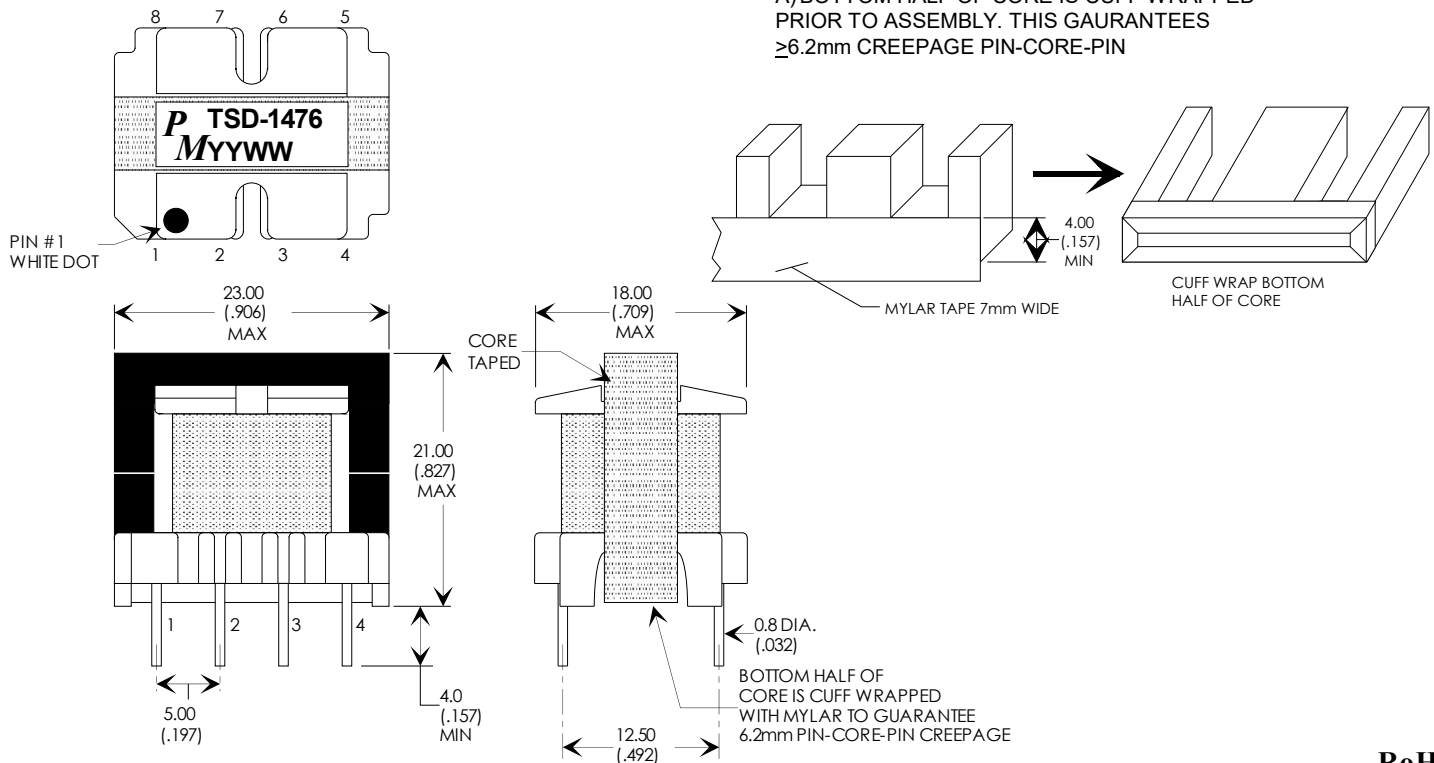
(1) REFER TO APPLICATION CIRCUIT OF FIGURE 3.

FIGURE 1: SCHEMATIC DIAGRAM



NOTE1:
REINFORCED INSULATION SYSTEM, UL1950, IEC950, CSA-950:
A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS
B) DESIGNED TO MEET ≥6.2mm CREEPAGE REQUIREMENTS.
C) VARNISH FINISHED ASSEMBLY.

FIGURE 2: PHYSICAL DIMENSIONS mm (INCHES)



NOTE2:
A) BOTTOM HALF OF CORE IS CUFF WRAPPED PRIOR TO ASSEMBLY. THIS GAURANTEES ≥6.2mm CREEPAGE PIN-CORE-PIN

RoHS



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES DIMENSIONAL TOLERANCES ARE:
DECIMALS ANGLES
.XX ± .05 ± 0° 30'
.XXX ± .010
DO NOT SCALE DRAWING

TRANSFORMER CONTROL DRAWING	
PREMIER P/N: TSD-1476	REVISION: 02/01/02
DRAWN BY: PETER PHAM	REF: PWR-TOP202YAI
SCALE: NONE	SHEET: 1 OF 1