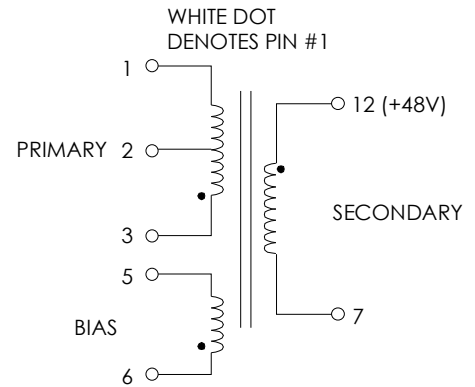


TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C
SWITCHING TRANSFORMER DESIGNED FOR USE WITH TOP 246

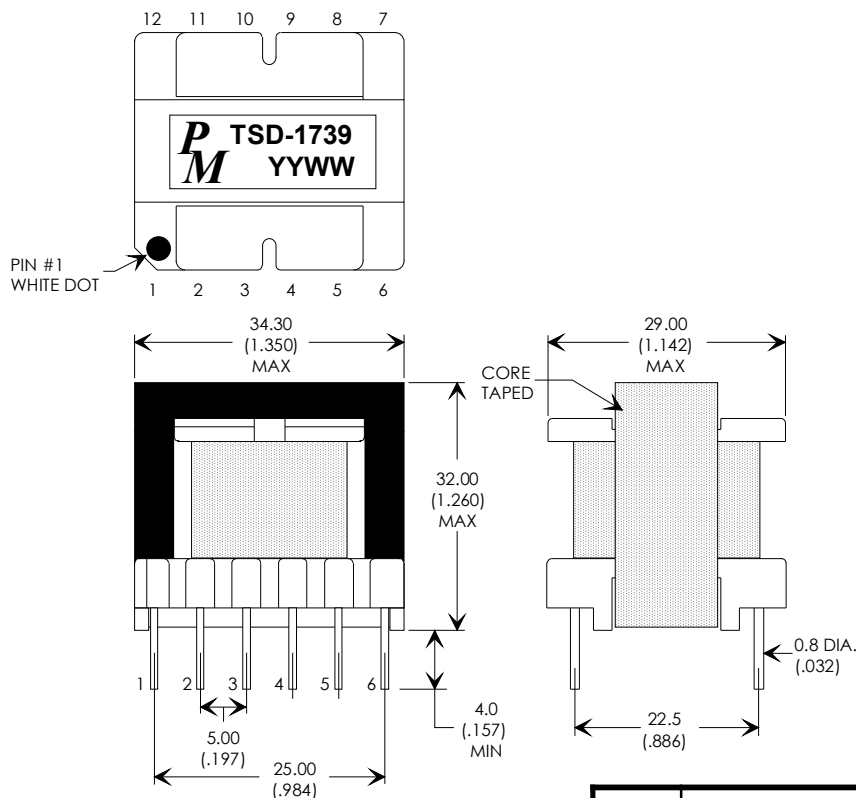
PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
PRIMARY INDUCTANCE (3-1) 0.250Vrms @ 100 KHZ	495	550	605	μHY
TURN RATIO'S: SEC. (12-7) : PRIMARY (3-1) BIAS (6-5) : PRIMARY (3-1)	-----	1:3.82 1:10.5	-----	± 3% ± 3%
PRI-LEAKAGE-IND (SEC'S SHORTED)	-----	-----	10.0	μHY
HI-POT: SECONDARY TO PRIMARY SECONDARY TO BIAS BIAS TO PRIMARY	4000 4000 1500	----- ----- -----	----- ----- -----	Vrms Vrms Vrms
APP CIRCUIT PARAMETERS: F _{sw} AC INPUT SECONDARY IS REGULATED: OUTPUT VOLTAGE OUTPUT CURRENT CONTINUOUS BIAS OUTPUT VOLTAGE BIAS OUTPUT CURRENT	----- 200 ----- ----- ----- -----	132 240 48.0 2.0 15 -----	----- 265 ----- ----- ----- 50	KHz Vdc Vdc Amps Vdc mA

FIGURE 1: SCHEMATIC DIAGRAM



NOTE1:
REINFORCED INSULATION SYSTEM (UL1950, IEC950):
A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS
B) 130°C REINFORCED INSULATION SYSTEM.
C) TRIPLE BASIC INSULATED SECONDARY.
D) DESIGNED FOR >6.2mm CREEPAGE/CLEARANCE.
E) VARNISH FINISHED ASSEMBLY.

FIGURE 2: PHYSICAL DIMENSIONS mm (INCHES)



REV.	DESCRIPTION OF CHANGES	BY
02/07/02	ORIGINAL RELEASE	LL
03/11/02	UPDATED RELEASE	LL



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN MM
DIMENSIONAL TOLERANCES ARE:
DECIMALS ANGLES
.X ± .25 ±0° 30'
.XX ± .15
DO NOT SCALE DRAWING

TRANSFORMER CONTROL DRAWING

PREMIER P/N: TSD-1739	REVISION: 03/11/02
DRAWN BY: LINH LE	REF: TOP 246
SCALE: NONE	SHEET: 1 OF 1