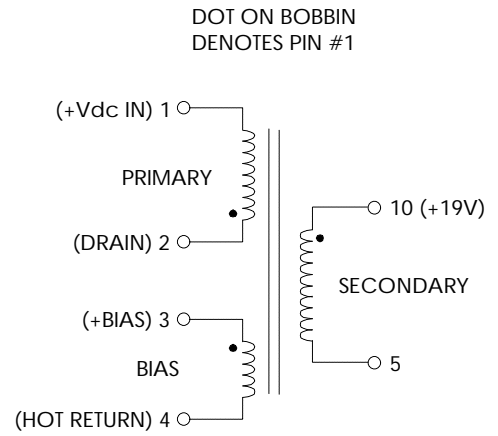


TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C

PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
PRIMARY INDUCTANCE (2-1) FREQ. = 100 KHZ @ 0.250Vrms	765	820	880	μHY
TURNRATIO'S: BIAS (3-4) : PRIMARY (2-1) SEC (10-5) : PRIMARY (2-1)	-----	1: 18.25 1: 5.62	-----	± 4% ± 4%
PRI LEAKAGE (10 THRU 5 SHORTED) FREQ. = 100 KHZ @ 0.250Vrms	-----	-----	30	μHY
HIPOT: PRIMARY TO SECONDARY BIAS TO SECONDARY	3000 3000	----- -----	----- -----	Vrms Vrms

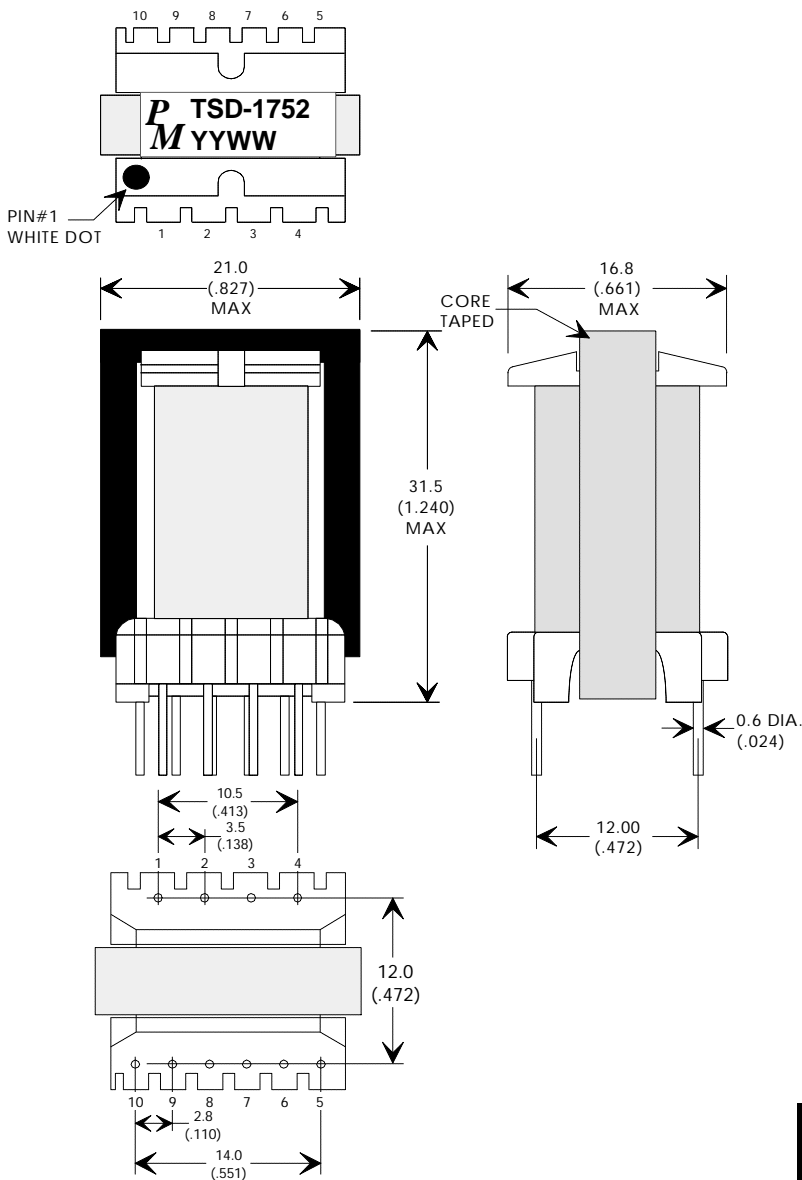
FIGURE 1: SCHEMATIC DIAGRAM



NOTE1:

- A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS
- B) TRIPLE BASIC INSULATED SECONDARY.
- C) VARNISH FINISHED ASSEMBLY.

FIGURE 2: PHYSICAL DIMENSIONS mm (INCHES)



REV.	DESCRIPTION OF CHANGES	BY
03/26/02	ORIGINAL RELEASE	PP
05/21/02	CORRECTED NUMBER OF TURNS ON SCHEMATIC	LL



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN MM
DIMENSIONAL TOLERANCES ARE:
DECIMALS ANGLES
.X ± .25 ± 0° 30'
.XX ± .15
DO NOT SCALE DRAWING

FLYBACK TRANSFORMER CONTROL DRAWING	
PREMIER P/N: TSD-1752	REVISION: 05/21/02
DRAWN BY: PETER PHAM	REF: TOP-232
SCALE: NONE	SHEET: 1 OF 3