TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C

PARAMETER	SP MIN.	ec <u>limits</u> typ.	MAX.	UNITS
PRIMARY INDUCTANCE (3-1) VOLTAGE = 0.250Vrms FREQUENCY = 100 KHZ	135	150	165	μНΥ
TURN RATIO'S: SEC #1 (14-13) : PRIMARY (3-1) SEC #2 (11-10) : PRIMARY (3-1) SEC #3 (9-8) : PRIMARY (3-1) BIAS (5-7) : PRIMARY (3-1)		1 : 4.40 1 : 7.33 1 : 22.0 1: 7.33		± 4% ± 4% ± 4% ± 4%
PRI LEAKAGE IND. (SEC SHORTED) 0.250Vrms @ 100 KHZ			TDB	μHY
HIPOT: PRIMARY TO SECONDARIES BIAS TO SECONDARIES	3000 3000			Vrms Vrms

FIGURE 2: PHYSICAL DIMENSIONS mm (INCHES)





NOTE1:

A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS B) ALL MATERIAL RATED 130C OR BETTER. C) MARGIN TAPE TO MEET ≥6.4mm CREEPAGE REQUIREMENTS. D) VARNISH FINISHED ASSEMBLY.

$\stackrel{P}{M}$ TSD-1961 $\stackrel{YYWW}{M}$







UNLESS OTHERWISE SPECIFIED	FLYBACK TRANSFORMER CONTROL DRAWING				
DIMENSIONAL TOLERANCES ARE: DECIMALS ANGLES .X ± .25 ±0 ° 30' .XX ± .15 DO NOT SCALE DRAWING	PREM	1IER P/N: TSD-1961	REVISION: 03/01/04		
	DRAV	VN BY: PETER PHAM	REF: TOP-249		
	SCAL	E: NONE	SHEET: 1 OF 3		

ΒY

PP

DESCRIPTION OF CHANGES

ORIGINAL RELEASE,

REV

03/01/04

